

1        1. A circuit arrangement for combining a modulator and an automatic gain  
2 control (AGC) amplifier comprising:

3              a modulator having inputs for receipt of signal inputs (IN+, IN-) and means for  
4 converting the signal inputs into modulated output current signals (OP1, OP2, R3, R4;  
5 Q1, Q2, Q3, Q4), based upon a local oscillator reference signal (LO+, LO-); and

6              an automatic gain control (AGC) amplifier operatively connected to the  
7 modulated output current signals of the modulator and also connected to a supply  
8 voltage (Vcc), the AGC amplifier further having means for amplifying said output  
9 current signals for delivering the output current signals to one of two balanced loads  
10 (R1, R2; and corresponding opposite phase components); wherein the output current  
signals are re-used by the AGC amplifier.

1        2. A circuit arrangement as defined in claim 1, wherein the AGC amplifier  
2 has means for controlling the amount of the amplified output current signals to present  
3 to either one of the two balanced loads, wherein this control of the amount of the  
4 amplified output current signals is dependent upon a control voltage (VP, VM)  
5 presented to the AGC amplifier.

1        3. A circuit arrangement as defined in claim 2, wherein the modulator  
2 means for converting the signal inputs into modulated output current signals comprises:

3              means for converting the signal inputs from a voltage to a current (OP1, OP2,  
4 R3, R4) and a modulating circuit (Q<sub>1</sub>, Q<sub>2</sub>, Q<sub>3</sub>, Q<sub>4</sub>) that receives the output of the  
5 voltage to current converting means and the local oscillator reference signal (LO+,  
6 LO-) so as to modulate the current conversion of the signal inputs with the local  
7 oscillator reference signal.

1        4. A circuit arrangement as defined in claim 1, wherein the modulator  
2 means for converting the signal inputs into modulated output current signals comprises:

3              means for converting the signal inputs from a voltage to a current (OP1, OP2,  
4 R3, R4) and a modulating circuit (Q<sub>1</sub>, Q<sub>2</sub>, Q<sub>3</sub>, Q<sub>4</sub>) that receives the output of the  
5 voltage to current converting means and the local oscillator reference signal (LO+,

6 LO-) so as to modulate the current conversion of the signal inputs with the local  
7 oscillator reference signal.

1 5. A circuit arrangement as defined in claim 1, further comprising:  
2 a non-inverting gain stage connected to the signal inputs (IN+, IN-) and having  
3 outputs connected to the inputs of the modulator.

1 6. A circuit arrangement as defined in claim 1, further comprising:  
2 an inverting gain stage having inputs connected to the signal inputs (IN+, IN-)  
3 and having outputs connected to the inputs of the modulator.

1 7. A circuit arrangement as defined in claim 1, further comprising:  
2 an inverting gain stage with common mode feedback having inputs connected to  
3 the signal inputs (IN+, IN-) and having outputs connected to the inputs of the  
modulator.

1 8. A circuit arrangement as defined in claim 1, wherein the modulator  
means for converting the signal inputs into modulated output current signals comprises  
2 current mirrors with emitter transistors (Q1, Q2, Q3, Q4) and resistors (R3, R4)  
3 connected to the emitters of the transistors, wherein  
4 the current mode input signals (IN+, IN-) are signals presented to a current to voltage  
5 converter including reference resistors ( $R_{ref1}$ ,  $R_{ref2}$ ) for presenting the signal inputs  
6 (IN+, IN-) to the modulator based upon the voltage drop across the respective  
7 reference resistors, the current to voltage converter with the associated reference  
8 resistors compensating for variations in resistance values of the modulator emitter  
9 resistors (R3, R4).  
10

1 9. A circuit arrangement as defined in claim 8, wherein the variation in  
2 percentage resistance values for the reference resistors ( $R_{ref1}$ ,  $R_{ref2}$ ) and the modulator  
3 emitter resistors (R3, R4) are substantially the same.

1           10. A circuit arrangement as defined in claim 1, wherein the means for  
2 converting the signal inputs include operational amplifiers (OP1, OP2), wherein the  
3 inputs of the modulator are positive inputs to the operational amplifiers, and further  
4 wherein the circuit arrangement comprises:

5           local oscillator filters each connected to a negative input of one of the  
6 operational amplifiers associated with the modulator; and

7           noise filters each having an input connected to the output of one of the  
8 operational amplifiers (OP1, OP2) and having a filtered output connected to the  
9 modulator.

1           11. A circuit arrangement as defined in claim 1, wherein the means for  
2 converting the signal inputs include operational amplifiers (OP1, OP2), wherein the  
3 inputs of the modulator are positive inputs to the operational amplifiers, and further the  
4 circuit arrangement comprises:

5           noise filters each connected to an output of one of the modulator operational  
6 amplifiers (OP1, OP2); and

7           a parallel input stage connected to the output of the noise filters, the parallel  
8 input stage comprising circuitry substantially the same as the modulator circuitry but  
9 without connection to the local oscillator reference signal.

1           12. A transmitter for use in a mobile communications apparatus comprising:  
2           a digital to analog (D/A) converter (20) for converting digital information into  
3 an analog signal input;

4           a modulator (24) operatively connected to the D/A converter (20) for  
5 modulating the signal inputs with a modulating signal so as to generate a modulated  
6 output signal;

7           an automatic gain control (AGC) amplifier (30) operatively connected to the  
8 modulator for controlling the gain of the modulated output signal; and

9           an amplifier (34) operatively connected to the output of the AGC amplifier for  
10 amplifying the gain controlled modulated output signal;

11           wherein the modulator (24) and the AGC amplifier (30) are combined to form a  
12 circuit arrangement (36), and

13           wherein the modulator has inputs for receipt of the signal inputs (IN+, IN-)  
14 and means for converting the signal inputs into modulated output current signals (OP1,  
15 OP2, R3, R4; Q1, Q2, Q3, Q4) based upon a local oscillator reference signal (LO+,  
16 LO-); and

17           wherein the AGC amplifier is operatively connected to the modulated output  
18 current signals of the modulator and also connected to a supply voltage (Vcc), the  
19 AGC amplifier further having means for amplifying said output current signals for  
20 delivering the output current signals to one of two balanced loads (R1, R2); wherein  
21 the output current signals are re-used by the AGC amplifier.

13.       A transmitter for use in a mobile communications apparatus as defined  
in claim 12, wherein the AGC amplifier has means for controlling the amount of the  
amplified output current signals to present to either one of the two balanced loads,  
wherein this control of the amount of the amplified output current signals is dependent  
upon a control voltage (VP, VM) presented to the AGC amplifier.

14.       A transmitter for use in a mobile communications apparatus, as defined  
in claim 13, wherein the modulator means for converting the signal inputs into  
modulated output current signals comprises:

4           means for converting the signal inputs from a voltage to a current (OP1, OP2,  
5 R3, R4) and a modulating circuit (Q<sub>1</sub>, Q<sub>2</sub>, Q<sub>3</sub>, Q<sub>4</sub>) that receives the output of the  
6 voltage to current converting means and the local oscillator reference signal (LO+,  
7 LO-) so as to modulate the current conversion of the signal inputs with the local  
8 oscillator reference signal.

1           15.      A transmitter for use in a mobile communications apparatus as defined  
2 in claim 12, wherein the modulator means for converting the signal inputs into  
3 modulated output current signals comprises:

4           means for converting the signal inputs from a voltage to a current (OP1, OP2,  
5 R3, R4) and a modulating circuit (Q<sub>1</sub>, Q<sub>2</sub>, Q<sub>3</sub>, Q<sub>4</sub>) that receives the output of the  
6 voltage to current converting means and the local oscillator reference signal (LO+,

7 LO-) so as to modulate the current conversion of the signal inputs with the local  
8 oscillator reference signal.

1 16. A transmitter for use in a mobile communications apparatus as defined  
2 in claim 12, further comprising:

3 a non-inverting gain stage connected to the signal inputs (IN+, IN-) and having  
4 outputs connected to the inputs of the modulator.

1 17. A transmitter for use in a mobile communications apparatus as defined  
2 in claim 12, further comprising:

3 an inverting gain stage having inputs connected to the signal inputs (IN+, IN-)  
4 and having outputs connected to the inputs of the modulator.

1 18. A transmitter for use in a mobile communications apparatus as defined  
2 in claim 12, further comprising:

3 an inverting gain stage with common mode feedback having inputs connected to  
the signal inputs (IN+, IN-) and having outputs connected to the inputs of the  
modulator.

1 19. A transmitter for use in a mobile communications apparatus as defined  
2 in claim 12, wherein the modulator means for converting the signal inputs into  
3 modulated output current signals comprises current mirrors with emitter transistors  
4 (Q1, Q2, Q3, Q4) and resistors (R3, R4) connected to the emitters of the transistors,  
5 wherein the current mode signal inputs (IN+, IN-) are presented to a current to  
6 voltage converter including reference resistors ( $R_{ref1}$ ,  $R_{ref2}$ ) for presenting the signal  
7 inputs (IN+, IN-) to the modulator based upon the voltage drop across the respective  
8 reference resistors, the current to voltage converter with the associated reference  
9 resistors compensating for variations in resistance values of the modulator emitter  
10 resistors (R3, R4).

1 20. A transmitter for use in a mobile communications apparatus as defined  
2 in claim 19, wherein the variation in percentage resistance values for the reference

3           resistors ( $R_{ref1}$ ,  $R_{ref2}$ ) and the modulator emitter resistors ( $R3$ ,  $R4$ ) are substantially the  
4           same.

1           21. A transmitter for use in a mobile communications apparatus as defined  
2           in claim 12, wherein the means for converting the signal inputs include operational  
3           amplifiers (OP1, OP2), wherein the inputs of the modulator are positive inputs to the  
4           operational amplifiers, and further wherein the circuit arrangement comprises:

5           local oscillator filters each connected to a negative input of one of the  
6           operational amplifiers associated with the modulator; and

7           noise filters each having an input connected to the output of one of the  
8           operational amplifiers (OP1, OP2) and having a filtered output connected to the  
9           modulator.

1           22. A transmitter for use in a mobile communications apparatus as defined  
2           in claim 12, wherein the means for converting the signal inputs include operational  
3           amplifiers (OP1, OP2), wherein the inputs of the modulator are positive inputs to the  
4           operational amplifiers, and further the circuit arrangement comprises:

5           noise filters each connected to an output of one of the modulator operational  
6           amplifiers (OP1, OP2); and

7           a parallel input stage connected to the output of the noise filters, the parallel  
8           input stage comprising circuitry substantially the same as the modulator circuitry but  
9           without connection to the local oscillator reference signal.